





Embedded Security Step-by-Step



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INSIDE Secure at a Glance



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INSIDE Secure at a Glance



Security IP Cores



- 600Gbps+ Cryptos
- Packet Engines
- Root-of-Trust Engine
- Public Key Engines
- FIPS 140-2
- Camouflage Tech
- Key Provisioning





Embedded Security Software



- TLS and DTLS
- IPsec, MACsec
- Secure Boot
- FIPS 140-2 Crypto Lib
- VPN, Data at Rest



App Protection and Payment



- Mobile Payment
- eWallet
- Healthcare apps
- Car Key Apps
- Multi-factor authentication





Content Protection



- DRMs Leadership:
 - > OTT CE devices
 - Mobile embedded
 - Downloadable DRMs
- HDCP and DTCP stacks
- Studio-Approved

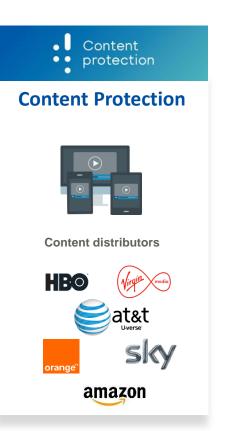


Supporting World Top Companies















Before We Go any further Few Announcements:

Inside Secure introduce
 its Root-of-Trust Family
 Programmable Root-of-Trust core



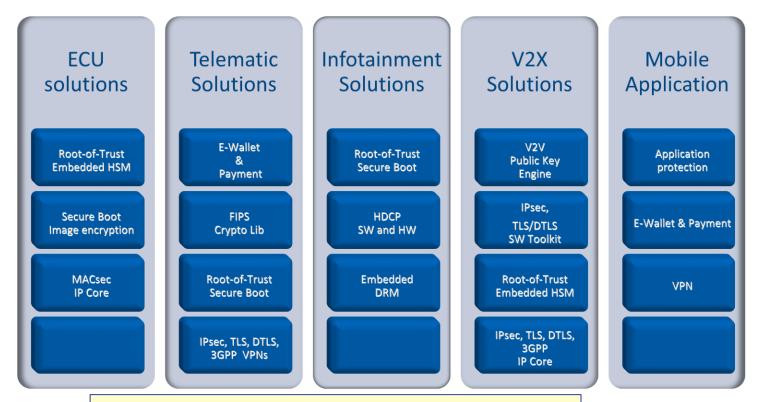
2. Inside Secure Acquired SypherMedia Offering:





- Largest 3rd Party Key Provisioning System
- Silicon Camouflage

Solutions for Automotive Market



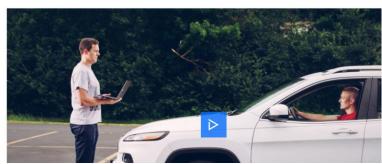
Check it out on https://www.insidesecure.com/Markets/Automotive



Hacking Jeep – Case Study

Problem	Think about it
Jeep Cherokee	Your IoT just the same
Open telnet port No authentication	Close ports or Enforce SSH auth
Bad random password	Use HW TRNG
FW Image analyzed	Use Image Encryption
FW Image modified	Use Secure Boot
CAN Bus allows full control to any sender	Use protocol with mutual-authentication
No OTA FW Upgrade	1.4M thumb drives sent to customers

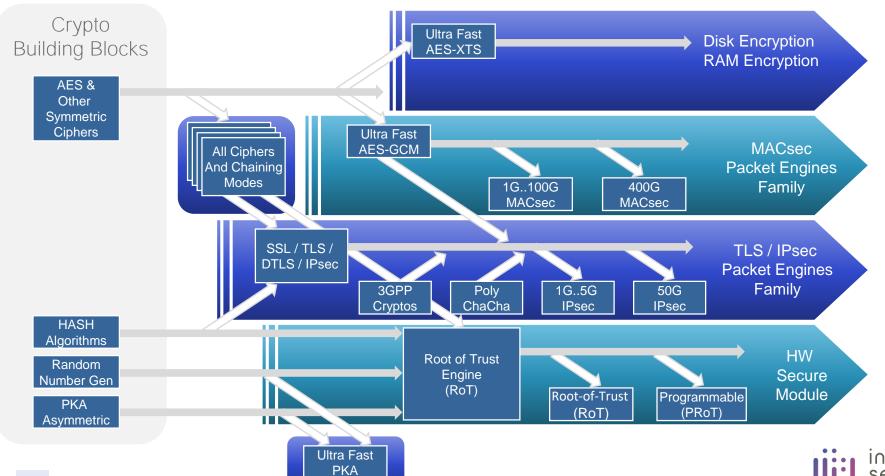
HACKERS REMOTELY KILL A JEEP ON THE HIGHWAY—WITH ME IN IT





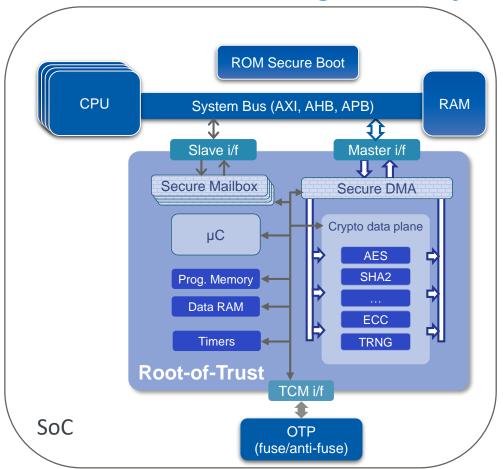


Inside Secure IP Cores Portfolio (more than 500 configurations)





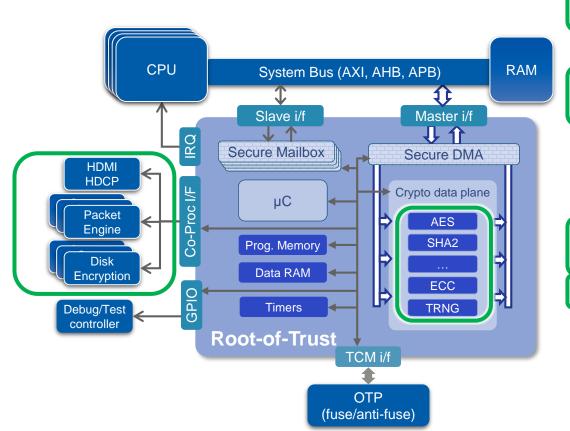
Embedding Security Step by Step



- Secure Boot
- SHA2 → Integrity
- ECC (Elliptic-Curve) → Authentication
- OTP (One Time Programmable)
 - > Immutable keys and state
- AES → Confidentiality
- Micro-Controller for flexibility
- Isolation of internal address space using Mailboxes
- TRNG and other CryptoEngines
- Integrate everything into IP Core



Root-of-Trust Swiss Army Knife



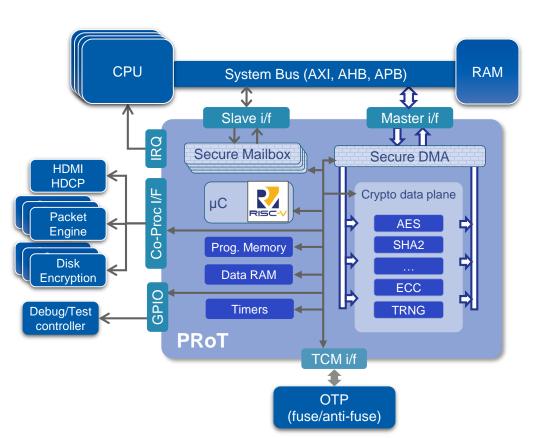
FIPS-140-2 level 2 certified



- Secure Boot
- Side Channel Protection
- Anti Tampering
- HW Protection for keys
 - Even if Kernel/HV/TEE breached
 - Anti Cloning
- Scalable Crypto Accelerators
 - Internal and External
- Secure debug enablement
- Built-in Key Provisioning
- Life-cycle management



Programmable Root-of-Trust



- Addition of Risc-V core to the secure perimeter
- Enables OEM to
 - Develop proprietary code
 - ➤ In-field SW upgrade
 - Manage Secure Boot
 - Terminate TLS inside the PRoT and support TLS Device Authentication
- Standard toolchain
- Potential enhancement with Secure flash for
 - Secure Element profile
 - Evita Full profile





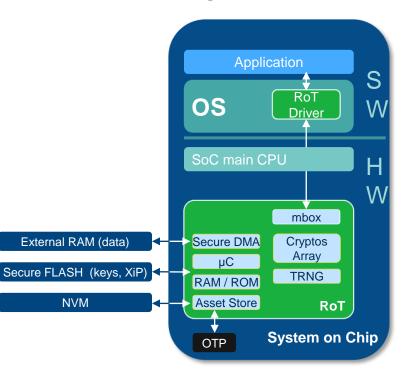
External RAM (data)

NVM

RoT Scales Across your Portfolio

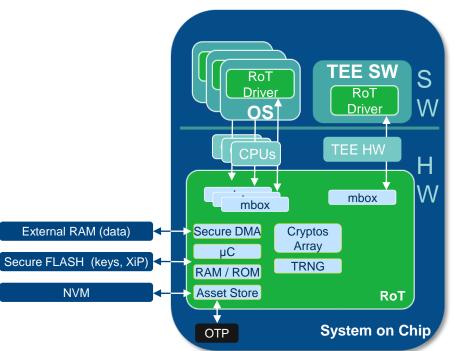
SoC without TEE

e.g. Micro controllers



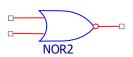
SoC with TEE and multiple CPUs

e.g. Mobile phones



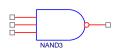


Conventional NOR2





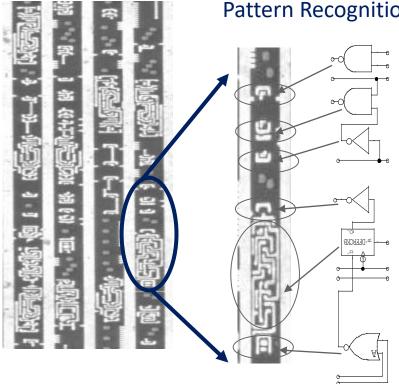
Conventional NAND3



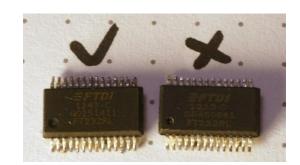
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Cell Camouflage

Reverse Engineering using Pattern Recognition



Layout → Netlist



Identical Counterfeit, at lower quality and price:

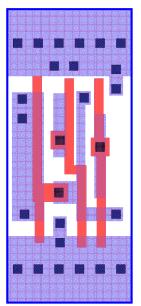
- 1. Consume market share
- 2. Damage Brand
- 3. Lower margin
- 4. Support and recalls



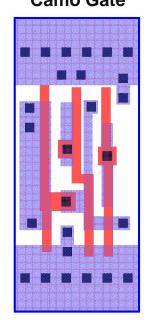
Foundry Standard Cells vs Camo Cells

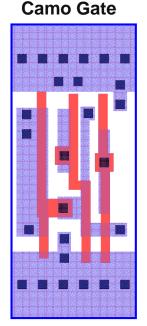
Camo cells are designed to appear as foundry cells, but perform different logical functions

Foundry Standard AND2 Gate



Inside Secure Ver1 Inside Secure Ver2
Camo Gate
Camo Gate

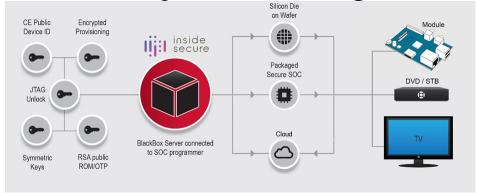




AND2 lookalike gates perform alternate functions



BlackBox Key Provisioning Overview



- Provisioning at Silicon stage, OEM stage and On-boarding stage
- Largest 3rd Party Key Provisioning with more that 60 OEMs
- And 13 leading SoC Vendors















ABS@CBN













BANG & OLUFSEN

SRSCRYPTO

























infomir (m)



INTERNATIONAL DATACASTING



Attack Landscape & RoT

RoT-s+ RoT-s Camo Cells RoT Side Channel Analysis **Chip Tampering** Logical **Fault Injection** (SCA) (Physical) Hostile SW Power glitch Probing & modifying Timing Attack (FIB, e-beam) • Power & EM Clock glitch Replay radiation analysis Optical reverse Buffer overflow Electromagnetic (SPA/DPA) engineering pulse injection Laser

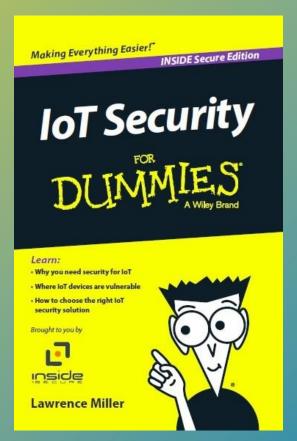
Non-invasive

Semi-invasive

Invasive

Cost & Expertise





Thank You!

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